

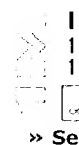
	Type	L #	Hits	Search Text	DBs
1	BRS	L9	256	712/201,25,26,27.ccls .	USPA T
2	BRS	L10	2639	data adj driven	USPA T
3	BRS	L12	1434	(data adj driven)and g06f\$.ipc.	USPA T
4	BRS	L13	258	(data adj driven)and g06f\$.ipc. and token	USPA T

	Type	Hits	Search Text	DBs
1	BRS	1	5781750.pn.	USPAT
2	BRS	1	5826089.pn.	USPAT
3	BRS	1	5890006.pn.	USPAT
4	BRS	1	5961632.pn.	USPAT
5	BRS	266	712/201	USPAT
6	BRS	221	712/201,25,27.ccls.	USPAT
7	BRS	121	712/201.ccls.	USPAT
8	BRS	10	712/201,25,27.ccls. and (data adj request)	USPAT
9	BRS	1	712/201,25,27.ccls. and (data adj packet adj request)	USPAT
10	BRS	4	712/201,25,27.ccls. and (data near2 record)	USPAT
11	BRS	238	712/201,25,27.ccls.	USPAT; US-PGPUB
12	BRS	57	712/201,25,27.ccls. and (data adj packet)	USPAT
13	BRS	34	712/201,25,27.ccls. and (record)	USPAT
14	BRS	4	712/201,25,27.ccls. and (request with record)	USPAT
15	BRS	8	4270167.pn. or 4494188.pn. or 5568617.pn. or 6256696.pn. or 5333320.pn. or 5918063.pn. or 5926643.pn. or 5465368.pn.	USPAT
16	BRS	101	popescu.in.	USPAT
17	BRS	0	popescu.in. and metaflow	USPAT
18	BRS	12	popescu.in. and shelf	USPAT
19	BRS	9	popescu.in. and shelves	USPAT
20	BRS	0	popescu.in. and dris	USPAT
21	BRS	16	popescu.in. and processor	USPAT
22	BRS	112	712/201,25,27.ccls. and (full or empty)	USPAT
23	BRS	9	712/201,25,27.ccls. and ((full or empty) ADJ (buffer or queue))	USPAT
24	BRS	9	712/201,25,27.ccls. and ((full or empty) ADJ (buffer or queue or(reservation adj station)))	USPAT

	Type	Hits	Search Text	DBs
25	BRS	16	712/?ccls. and ((full or empty) ADJ (buffer or queue or(reservation adj station)))	USPAT
26	BRS	27	712/?ccls. and ((full or empty) ADJ (buffer or queue or(reservation adj station)or signal))	USPAT
27	BRS	557	emma.in.	USPAT
28	BRS	0	emma.in. and 712/?ccls.	USPAT
29	BRS	0	emma.in. and 712/?ccls. and branch	USPAT
30	BRS	0	emma.in. and 712/?ccls. and branch	USPAT
31	BRS	0	emma.in. and 712/?ccls.	USPAT
32	BRS	11	emma.in. and 712/\$.ccls.	USPAT
33	BRS	20	morton.in. and 712/\$.ccls.	USPAT
34	BRS	1	6038656.pn.	USPAT
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Gaudiot, J.-L.; Wei, Y.-H.;

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Jennings, G.;

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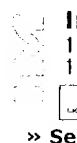
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1 Design and performance of a modular hardware process mapper (scheduler) for a real-time token controlled data driven multiprocessor architecture
Heath, J.R.; Ramamoorthy, S.;

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2 Data-driven parallel production systems: a macro actor/token implementation
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